**Design Project**

**Pseudo-Random Bit Sequence Generator**

**Prepared by:**

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***For Access to Files Not Included with This Report***

***(i.e., Schematics, Layouts, Waveforms, etc.) See:***

***https://github.com/red-135/ECE482\_DesignProject***

***(Files Maintained by Steven Blair, smblair2@illinois.edu)***

**ECE 482**

**Digital IC Design**

**December 7, 2016**